

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning at page 3, line 10, which starts with "As shown in FIG. 1, two data lines" with the following rewritten paragraph:

A¹
As shown in FIG. 1, two data lines 11 are aligned parallel to each other, and two scanning lines or gate lines 12 are aligned parallel to each other in a perpendicular direction to the data lines. Furthermore, TFTs, including source/drain electrodes 13 and 14 and a gate [[line]] electrode 15, is formed at intersections of the data lines and the gate lines 12. At the center portion of the pixel area is formed a pixel electrode 16 electrically connected with the drain electrode 14. A Cst 17 is formed on an upper end of the pixel area using a portion of the gate wiring as an electrode. The Cst 17 helps to charge a signal voltage.

Please replace the paragraph beginning at page 5⁵ line 11, which starts with "It is, therefore, an object of the present invention" with the following rewritten paragraph:

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It is, therefore, an object of the present invention, to provide a liquid crystal device capable of blocking a transmission of light and maintaining a luminosity of a liquid crystal display panel uniformly by forming an active layer of amorphous ~~silicone~~ (a-Sh:H) silicon (a-Si:H) material on a lower layer of pixels controlled by a number 1 scanning line or gate line (G1).

Please replace the paragraph beginning at page 7, line 1⁷, which starts with "In the liquid crystal display described with respect to FIG. 4" with the following rewritten paragraph:

A³
In the liquid crystal display described with respect to FIG. 4, an active layer of, e.g., amorphous silicon (~~a-Si:H~~) (a-Si:H) material, on a lower layer of pixels controlled by the number one scanning line or gate line (G1) (see FIG. 2) serves to restrict a transmission of light to pixel electrodes and to restrict the line light phenomenon.

Please replace the paragraph beginning at page 7, line 14⁽¹⁴⁾, which starts with "A gate insulating film 52 is formed on a substrate 51, a first active layer" with the following rewritten paragraph:

A⁴
A gate insulating film 52 is formed on a substrate 51, a first active layer, which is a channel layer of the TFT, (not shown) is formed on the gate insulating film 52, and a second active layer 53 is formed on the gate insulating film corresponding to a portion where the pixel electrodes are formed. Here, the first and second active layers are preferably made of amorphous silicon (~~a-Si:H~~) (a-Si:H) material. First and second data lines 54 and 55 are patterned at right and left portions of the second active layer 53, and a passivation layer 56 of insulating film material is formed on the substrate 51 including the data lines. A plurality of pixel electrodes 57 are patterned on the passivation layer 56 corresponding to the active layer 53.

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Please replace the title with the following rewritten title:

LIQUID CRYSTAL DISPLAY WITH LIGHT TRANSMISSION ADJUSTING
LAYER AND MANUFACTURING METHOD OF THE SAME